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E. Hillis  
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

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Continuation of Serial No. 08/821,468  
filed March 21, 1997

Art Unit: 2858

Filing Date: April 30, 1999

Examiner: SOLIS, J.

Title: HYBRID INTERCONNECT AND SYSTEM  
FOR TESTING SEMICONDUCTOR DICE

Attorney Docket No.: 96-876.1

**INFORMATION DISCLOSURE STATEMENT**  
**April 30, 1999**

Assistant Commissioner of Patents  
BOX PATENT APPLICATION  
Washington, D.C. 20231

Sir:

The following sections are being submitted for this  
Information Disclosure Statement:

1.   x   Preliminary Statements.
2.   x   FORM PTO - 1449.
3.        Statement As to Information Material to  
Examination Not Found in Patents or Publications.
4.   x   Identification of Prior Application in Which Listed  
Information Was Already Cited and For Which No Copies  
are Submitted Or Need be Submitted.
5.        Cumulative Patents or Publications.
6.   x   Copies of Listed Information Items Accompanying  
This Statement.
7.        Concise Explanation of Non-English Language  
Listed Information Items.
8.        Translation(s) of Non-English Language Documents.

